

Chapter 6-1. PN-junction diode: I-V characteristics

Topics:

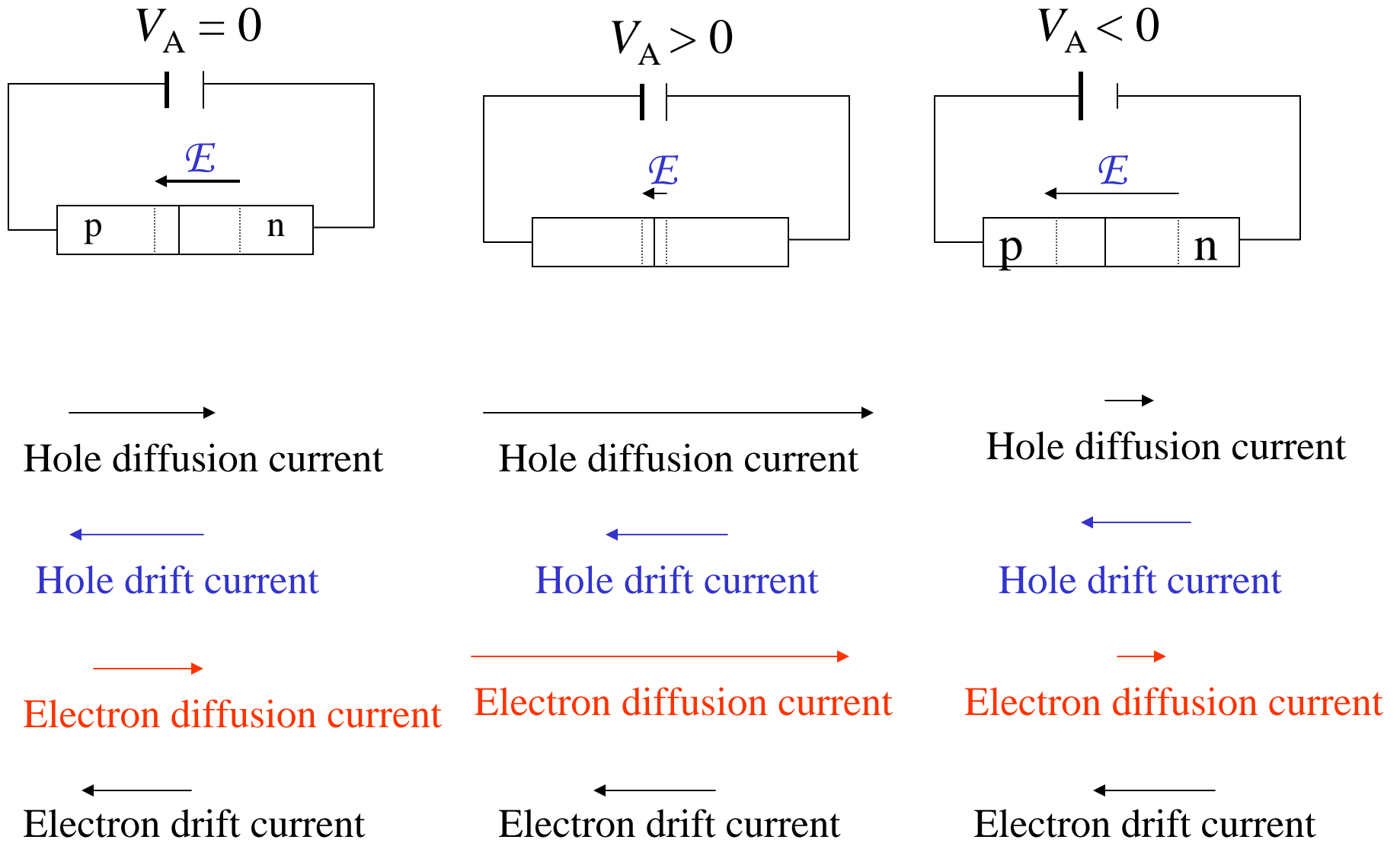
PN Junction under bias (qualitative discussion)

Ideal diode equation

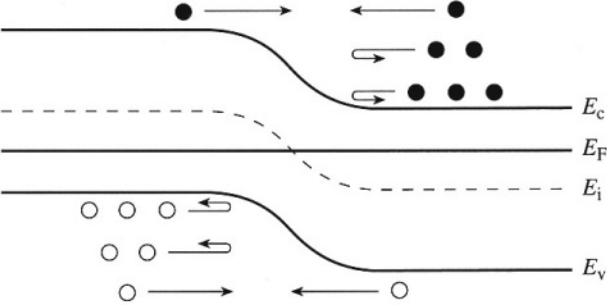
Deviations from the ideal diode

Charge-control approach

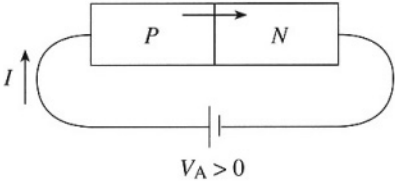
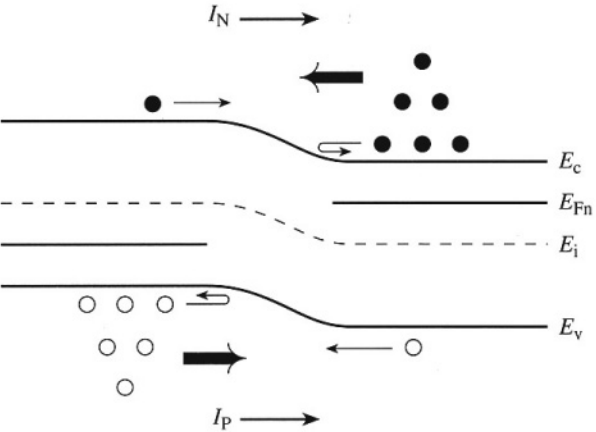
PN junction under various bias conditions



Band diagram and carrier flow under bias



(a) Equilibrium ($V_A = 0$)



(b) Forward bias ($V_A > 0$)

Fig. 6.1(Pierret, 1996)

Band diagram and carrier flow under bias

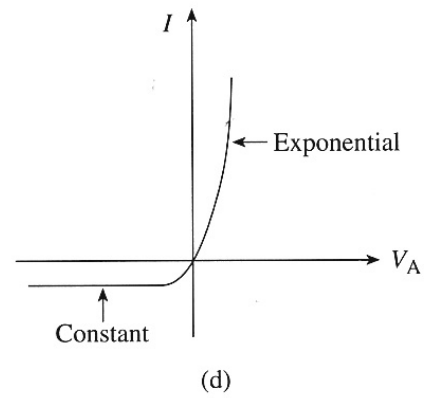
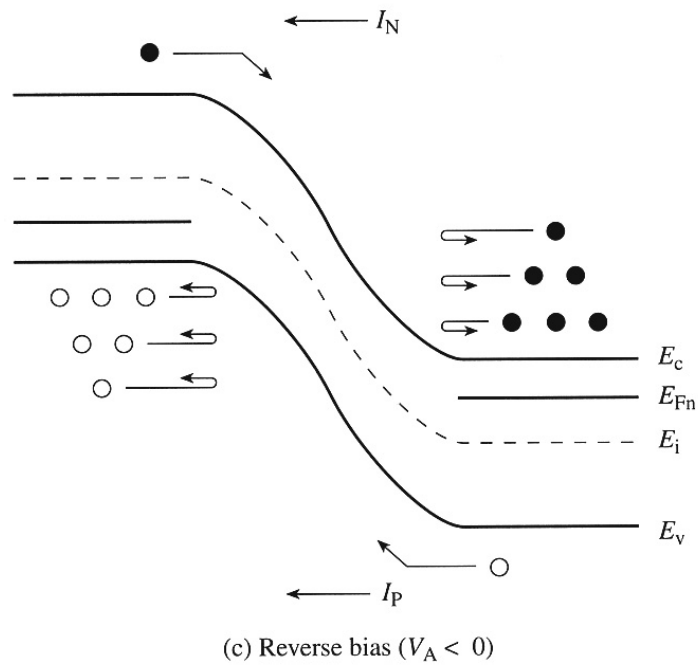


Figure 6.1 Continued.

Effect of bias on diffusion current

When the diode forward-bias-voltage is increased, **the barrier for electron and hole diffusion current decreases linearly**. See the band diagram.

Since the **carrier concentration decreases exponentially with energy** in both bands, diffusion current increases **exponentially** as the barrier is reduced.

As the reverse-bias-voltage is increased, **the diffusion current decreases rapidly to zero**, since the fall-off in current is exponential.

Effect of bias on drift current

When the reverse-bias-voltage is increased, **the net electric field increases, but drift current *does not* change.** In this case, drift current is limited **NOT** by **HOW FAST** carriers are swept across the depletion layer, but rather **HOW OFTEN**.

The number of carriers drifting across the depletion layer is **small** because the **number of minority carriers that diffuse towards the edge of the depletion layer is small.**

To a first approximation, the drift current **does not change** with the **applied voltage.**

Effect of bias on the “net” current

$|I_{\text{drift}}|$ does not change with applied voltage, V_A

$|I_{\text{diff}}|$ varies exponentially with applied voltage (Why?) $|I_{\text{diff}}| = I_0 \exp(V_A/V_{\text{ref}})$ where I_0 and V_{ref} are constants.

Net current = $I_{\text{diff}} - I_{\text{drift}}$

At equilibrium, $V_A = 0$; Net current = 0

$$|I_{\text{diff}}|_{V_A=0} = |I_{\text{drift}}|_{V_A=0} = I_0$$

At any applied voltage, V_A ,

$$I = I_0 e^{\frac{V_A}{V_{\text{ref}}}} - I_{\text{drift}} = I_0 (e^{\frac{V_A}{V_{\text{ref}}}} - 1)$$

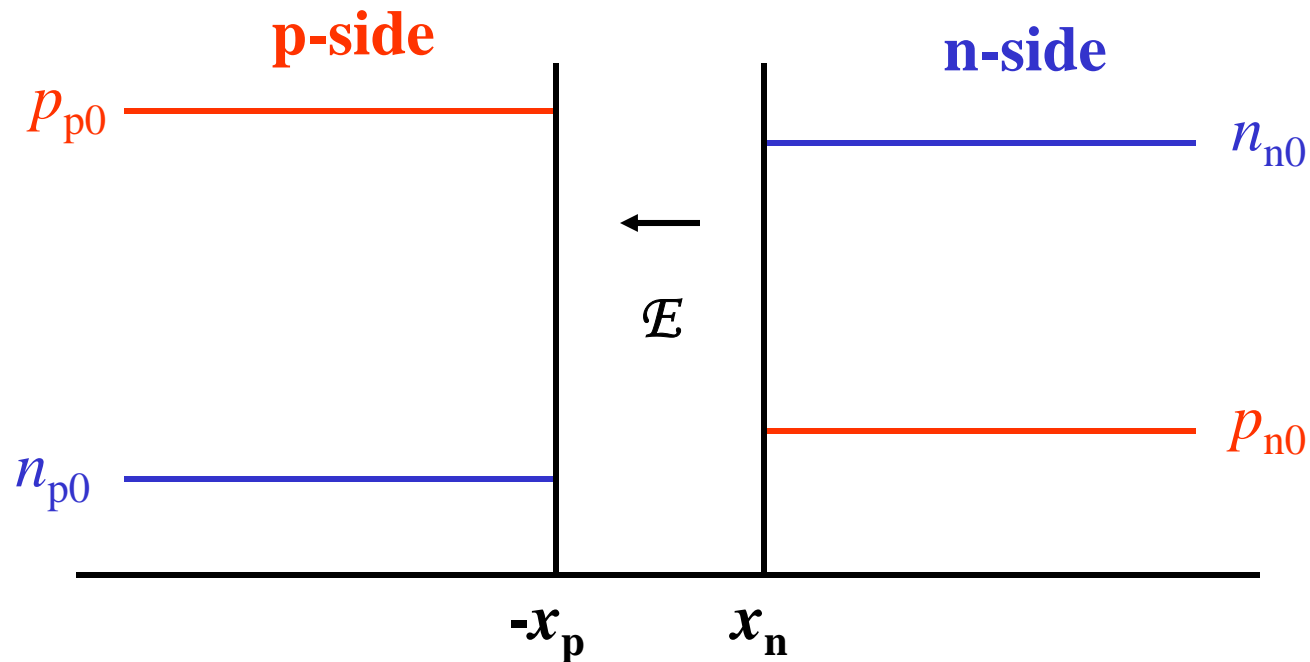
since $I_{\text{drift}} = I_0$ at any voltage.

Quantitative solution

Assumptions which must hold

- The diode is being operated under steady state conditions
- A non-degenerately doped step junction models the doping profile
- The diode is one-dimensional
- Low-level injection (conditions) prevail in the quasi-neutral regions
- There are no processes other than drift, diffusion, and thermal recombination-generation taking place inside the diode, $G_L=0$

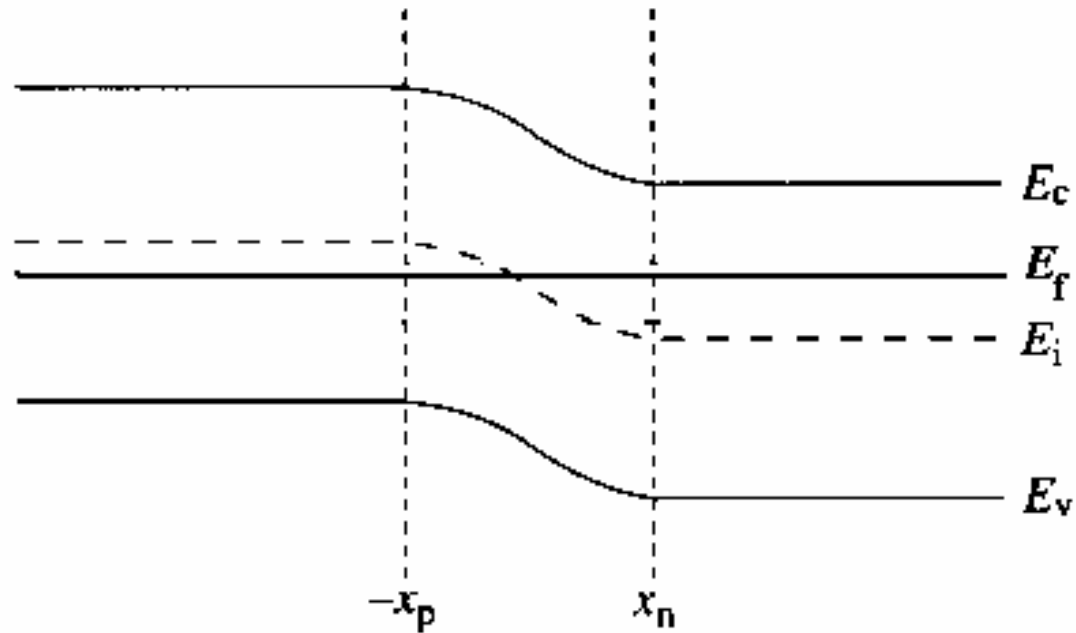
Majority and minority carrier concentration under equilibrium



Subscript "0" refers to equilibrium conditions

- Hole diffusion current
- ← Hole drift current
- Electron diffusion current
- ← Electron drift current

Relationship between carrier concentration and V_{bi}



$$\begin{aligned} V_{bi} &= (E_i - E_F)_{p\text{-side}} + (E_F - E_i)_{n\text{-side}} \\ &= \frac{kT}{q} \ln \left(\frac{p_{p0}}{n_i} \right) + \frac{kT}{q} \ln \left(\frac{n_{n0}}{n_i} \right) \end{aligned}$$

Relationship between carrier concentration and V_{bi}

$$\begin{aligned} V_{bi} &= \frac{kT}{q} \ln \left(\frac{p_{p0} n_{n0}}{n_i^2} \right) \\ &= \frac{kT}{q} \ln \left(\frac{p_{p0}}{p_{n0}} \right) \quad \text{because} \quad \frac{n_i^2}{n_{n0}} = p_{n0} \end{aligned}$$

$$\text{Therefore,} \quad \frac{p_{p0}}{p_{n0}} = e^{\frac{qV_{bi}}{kT}} \quad \text{and} \quad \frac{n_{n0}}{n_{p0}} = e^{\frac{qV_{bi}}{kT}}$$

$$\frac{\text{hole conc. on p - side}}{\text{hole conc. on n - side}} = e^{\frac{qV_{bi}}{kT}} = \frac{\text{electron conc. on n - side}}{\text{electron conc. on p - side}}$$

Strictly, these concentrations are at the depletion layer edge

Majority and minority carrier concentration under bias

When an external voltage is applied, the minority carrier concentration at the edge of the depletion layer will change. If a forward voltage ($V_A = \text{positive}$) is applied, the barrier will be lower and carrier injection (diffusion part) will increase. The minority carrier concentration at the edge of the depletion layer will increase.

If a reverse voltage ($V_A = \text{negative}$) is applied, the barrier for carrier injection (diffusion part) will increase, and the minority carrier concentration at the edge of the depletion layer will decrease.

The drift of minority carriers across the junction does not change much with applied voltage. Why?

At $V_A = 0$, the carrier injection and the drift of minority carriers cancel each other such that an equilibrium conc. is maintained.

If “low-level-injection” condition is assumed, then the majority carrier concentration will not change under any of the above conditions.

Relationship between carrier concentration and V_A

$$\frac{p_p}{p_n} = e^{\frac{q(V_{bi} - V_A)}{kT}} \quad \text{since } (V_{bi} - V_A) \text{ is the net voltage (or barrier) when a forward voltage is applied.}$$

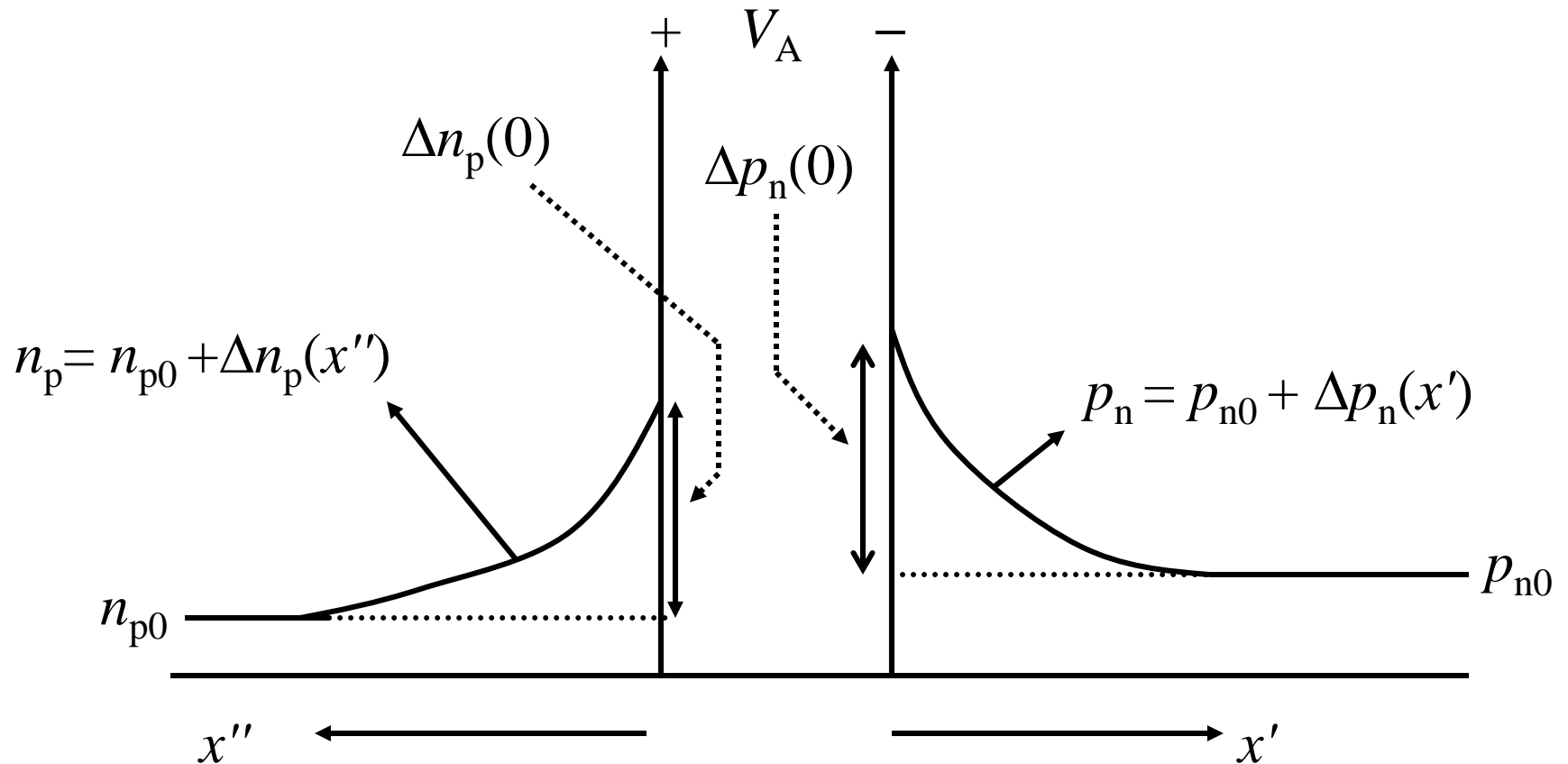
At low-level injection: $p_p = p_{p0}$; Recall that $\frac{p_{p0}}{p_{n0}} = e^{\frac{qV_{bi}}{kT}}$

$$p_n = p_{p0} e^{-\frac{q(V_{bi} - V_A)}{kT}} = p_{p0} e^{\frac{-qV_{bi}}{kT}} e^{\frac{qV_A}{kT}} = p_{n0} e^{\frac{qV_A}{kT}}$$

then

$$\frac{p_n}{p_{n0}} = e^{\frac{qV_A}{kT}} \quad \frac{n_p}{n_{p0}} = e^{\frac{qV_A}{kT}}$$

Minority carrier concentration profile under bias



$$\Delta n_p(x'') = \Delta n_p(0) e^{-\frac{x''}{L_n}}$$

$$\Delta p_n(x') = \Delta p_n(0) e^{-\frac{x'}{L_p}}$$

Relationship between applied voltage and excess minority carrier concentration

$$\frac{n_p(0)}{n_{p0}} = e^{\frac{qV_A}{kT}}$$

$$\frac{p_n(0)}{p_{n0}} = e^{\frac{qV_A}{kT}}$$

$$n_p(0) = n_{p0} e^{\frac{qV_A}{kT}}$$

$$p_n(0) = p_{n0} e^{\frac{qV_A}{kT}}$$

$$\Delta n_p(0) = n_p(0) - n_{p0}$$

$$= n_{p0} \left(e^{\frac{qV_A}{kT}} - 1 \right)$$

$$\Delta p_n(0) = p_n(0) - p_{n0}$$

$$= p_{n0} \left(e^{\frac{qV_A}{kT}} - 1 \right)$$